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## ABSTRACT OF THE DISCLOSURE

A timepiece structured to record and display the elapsed time from at least one specific event to the current time including a processor having a chronographic application structured to record the elapsed time on a continuous basis. The processor also includes a restrictive designation application structured to restrict resetting of the timepiece, or the chronographic application associated therewith, by a user thereof, with a different event. A memory capability is included for storing one or more original input times of associated events, wherein the timepiece and/or the chronographic application can be reset only by entry through a restrictive access to the memory capability, such as may be accomplished by the an authorized processing facility, rather than the user or owner of the timepiece. The elapsed time of a selected specific event is thereby "permanently" and continuously recorded for display by restricting the ability to reset or stop the timepiece.